

09727718-113000

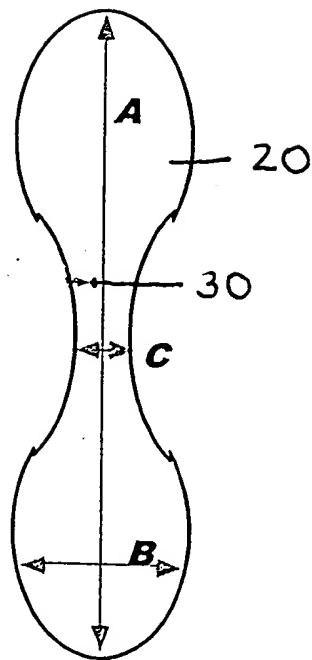


Figure 1

The circuit diagram shows a differential amplifier with a resistor network. The input stage consists of two NMOS transistors, Q1 and Q2, whose sources are connected to a common source node. This node is connected to a PMOS transistor Q3, which is biased by a current source (represented by a battery symbol) and a resistor R7. The gates of Q1 and Q2 are connected to a common gate node, which is biased by a current source and a resistor R8. The gates of Q1 and Q2 are also connected to a resistor network consisting of resistors R1, R2, R3, and R4. The output of the amplifier is taken from the drain of Q2, which is connected to a resistor R5 and a load resistor R6. The output voltage is labeled Vout. The circuit is powered by a positive supply +V and a negative supply -V.

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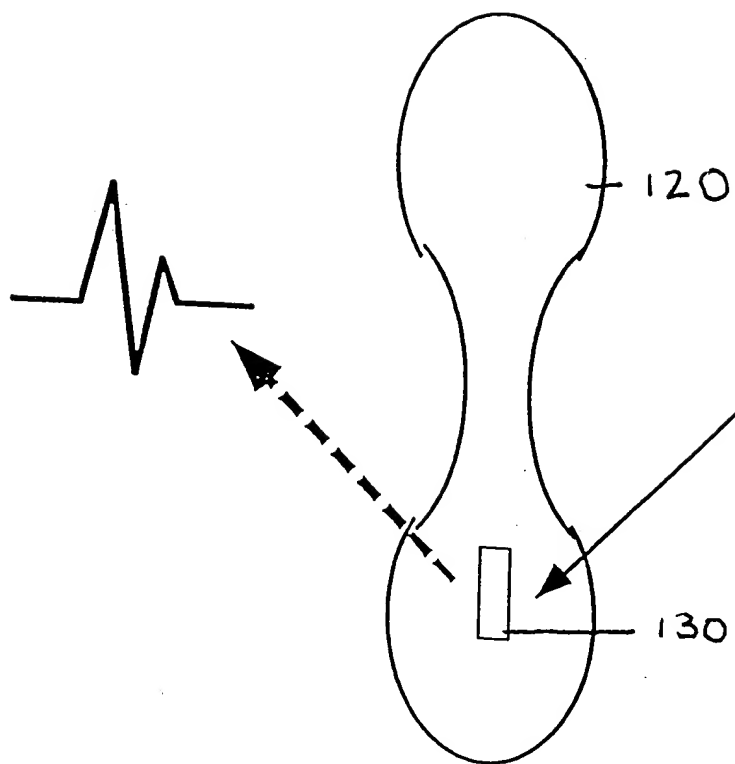


Figure 3

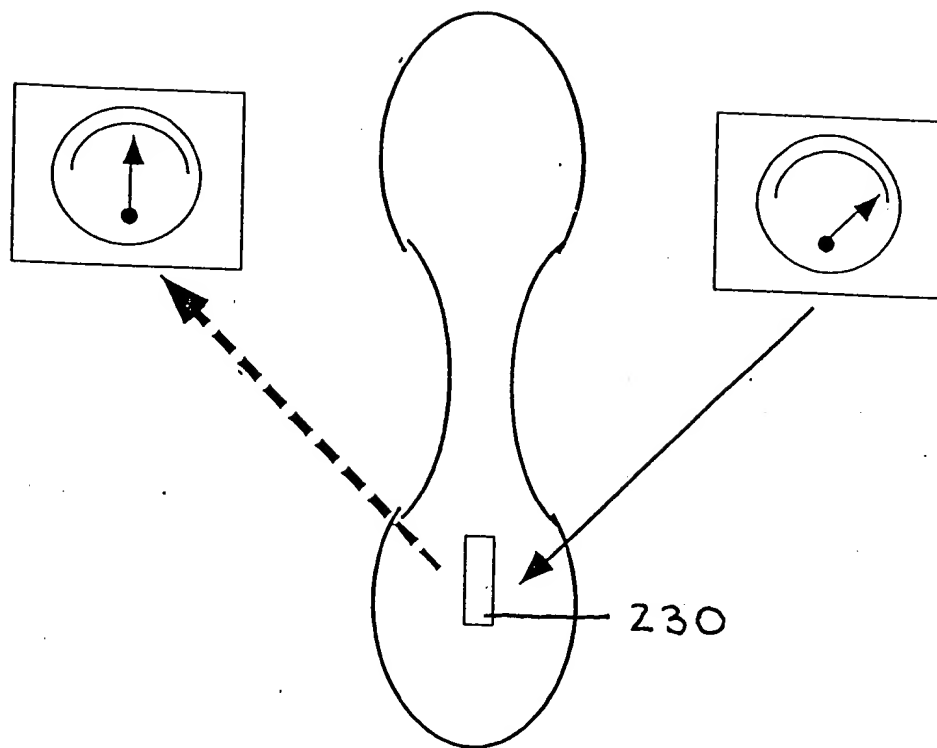


Figure 4

Figure 5

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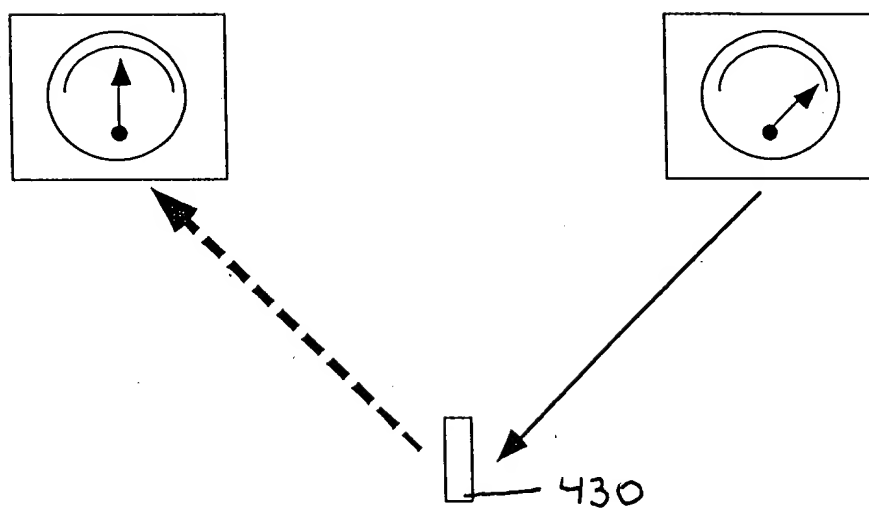


Figure 6

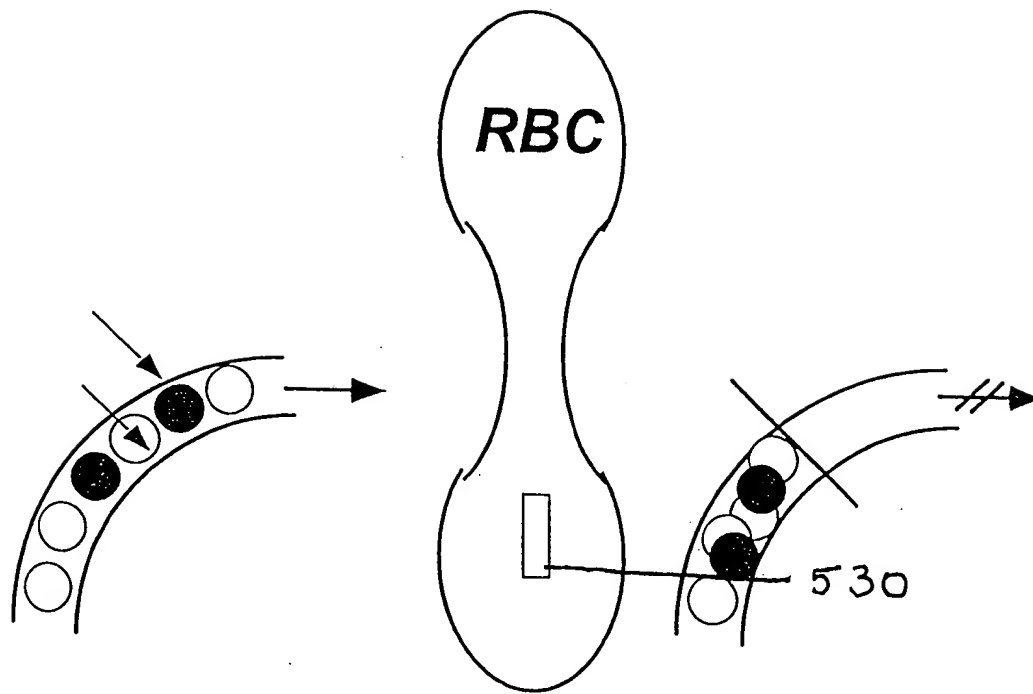


Figure 7

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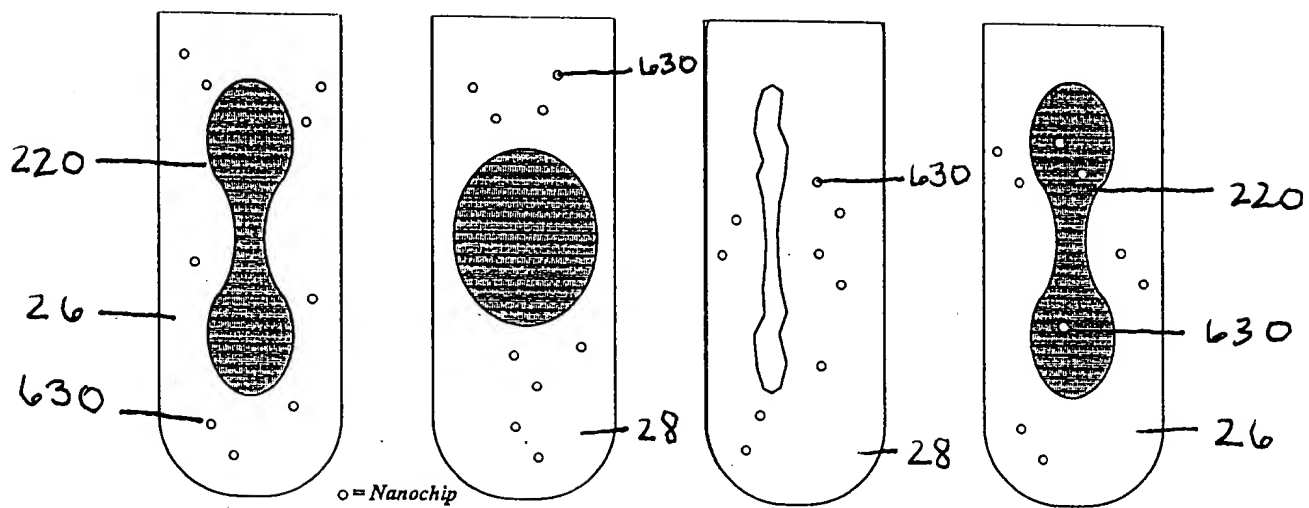


Figure 8



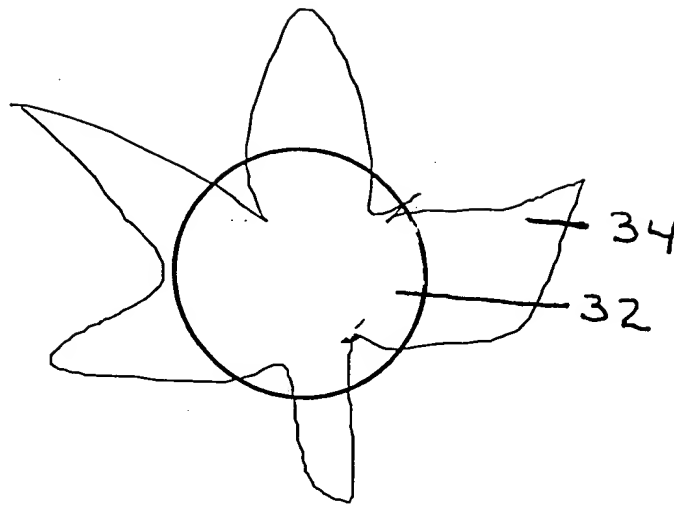


Figure 9

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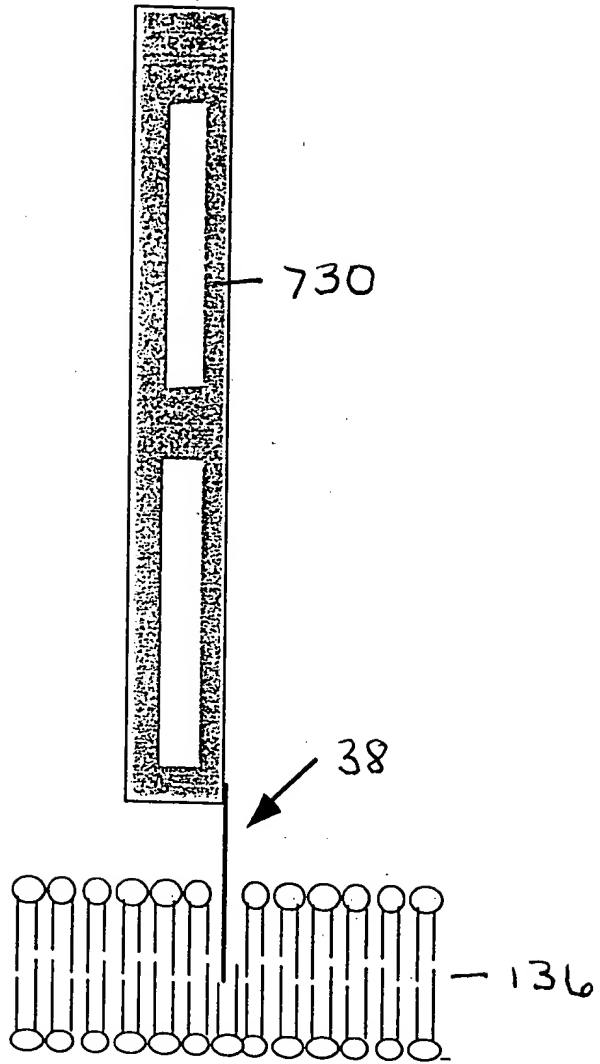


Figure 10

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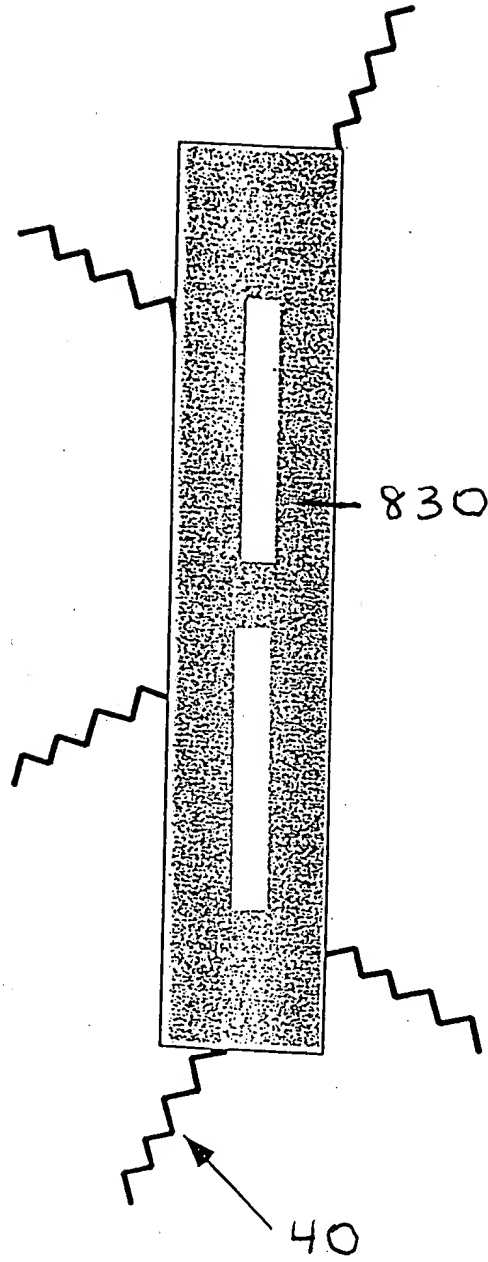


Figure 11